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28

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Akihiko EBINA et al.

Application No.: 10/052,255

Docket No.: 111778

Filed: January 23, 2002

FILE COPY

For: METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE INCLUDING NONVOLATILE SEMICONDUCTOR MEMORY
DEVICES

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REQUEST FOR CORRECTION OF PALM RECORDS

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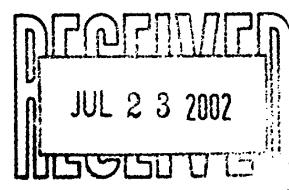
Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

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JAO:TJP/mlb

Date: June 6, 2002

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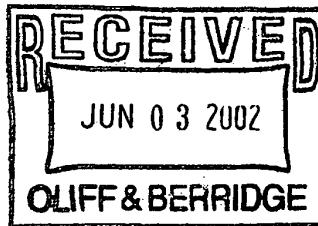
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/052,255	01/23/2002	2812	870	111778	11	15	1

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CONFIRMATION NO. 1930

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OC00000008212686

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Assignment For Published Patent Application

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Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2001-021931(P) 01/30/2001

If Required, Foreign Filing License Granted 02/19/2002

Projected Publication Date: 09/12/2002

Non-Publication Request: No

Early Publication Request: No

Title

Method of manufacturing semiconductor integrated circuit device including nonvolatile

semiconductor memory devices

Preliminary Class

438

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